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Term	Documents
(6 AND 14).PGPB,USPT.	0
(L6 AND L14).PGPB,USPT.	0

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result set

<u>L29</u>	16 and 114	0	<u>L29</u>
<u>L28</u>	16 and 113	0	<u>L28</u>
<u>L27</u>	16 and 111	0	<u>L27</u>
<u>L26</u>	16 and 19	6	<u>L26</u>
<u>L25</u>	16 and 17	19	<u>L25</u>
<u>L24</u>	13 and 114	0	<u>L24</u>
<u>L23</u>	13 and 113	1	<u>L23</u>
<u>L22</u>	13 and 111	7	<u>L22</u>
<u>L21</u>	13 and 19	17	<u>L21</u>
<u>L20</u>	13 and 17	111	<u>L20</u>
<u>L19</u>	15 and 114	8	<u>L19</u>
<u>L18</u>	15 and 113	4	<u>L18</u>
<u>L17</u>	15 and 111	7	<u>L17</u>

<u>L16</u>	15 and 19	12	<u>L16</u>
<u>L15</u>	15 and 17	62	<u>L15</u>
<u>L14</u>	(716/2-18)![CCLS]	9458	<u>L14</u>
<u>L13</u>	(710/305-317)![CCLS]	5367	<u>L13</u>
<u>L12</u>	(712/28-40)[CCLS]	2023	<u>L12</u>
<u>L11</u>	(712/28-40)[CCLS]	2023	<u>L11</u>
<u>L10</u>	(712/2-9)[CCLS]	472	<u>L10</u>
<u>L9</u>	(712/2-9)![CCLS]	472	<u>L9</u>
<u>L8</u>	(712/2-300)[CCLS]	12986	<u>L8</u>
<u>L7</u>	(712/2-300)![CCLS]	12986	<u>L7</u>

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L6</u>	L4 and 13	52	<u>L6</u>
<u>L5</u>	L4 and 12	415	<u>L5</u>
<u>L4</u>	(south or north) and (bridge or interconnect\$4 or crossbar\$1)	93778	<u>L4</u>
<u>L3</u>	L2 and coheren\$3 near12 (memor\$4 or stor\$4 or cach\$3)	312	<u>L3</u>
<u>L2</u>	(chip or chipset or IC) and vector and scalar	4632	<u>L2</u>
<u>L1</u>	(chip or chipset or IC) near45 vector near45 scalar	66	<u>L1</u>

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» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

- ☐ 1. A thread and data-parallel MPEG-4 video encoder for a system-on-chip multiprocessor
Jacobs, T.R.; Chouliaras, V.A.; Nunez-Yanez, J.L.;
[Application-Specific Systems, Architecture Processors, 2005. ASAP 2005. 16th IEEE International](#)
23-25 July 2005 Page(s):405 - 410
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